



PCEI (Parallel Count Enable Input) and SCEI (Serial Count Enable Input) must both be low before counting is enabled.

Clear input clears the counter but not the latch.

Clock is a high edge clock. LS (Latch Strobe) is a low enable to the D-Type latch.

Max Count Out would go to Clock In on the next higher digit.

RBI (Ripple Blanking In) comes from RBO (Ripple Blanking Out) of the next higher digit.

If the higher digit is zero blank the display. Ground RBI of the highest digit..

BI (Blanking In) blanks the digit.

Title		
G25577, TIL306 BCD counter, latch and display		
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